

UN205P23T

ROHS

P-Channel Enhancement Mode MOSFET

Product Summary

V_{DS}	-20V
I_D	-4.1A
$R_{DS(ON)}$ (@ $V_{GS}=-4.5V$ $I_D=-4.1A$)	$\leq 52m\Omega$
$R_{DS(ON)}$ (@ $V_{GS}=-2.5V$ $I_D=-3A$)	$\leq 75m\Omega$

Features

- ◆ Advanced Trench Process Technology
- ◆ Low Threshold Voltage
- ◆ Fast Switching Speed
- ◆ Halogen-Free & Lead-Free

Applications

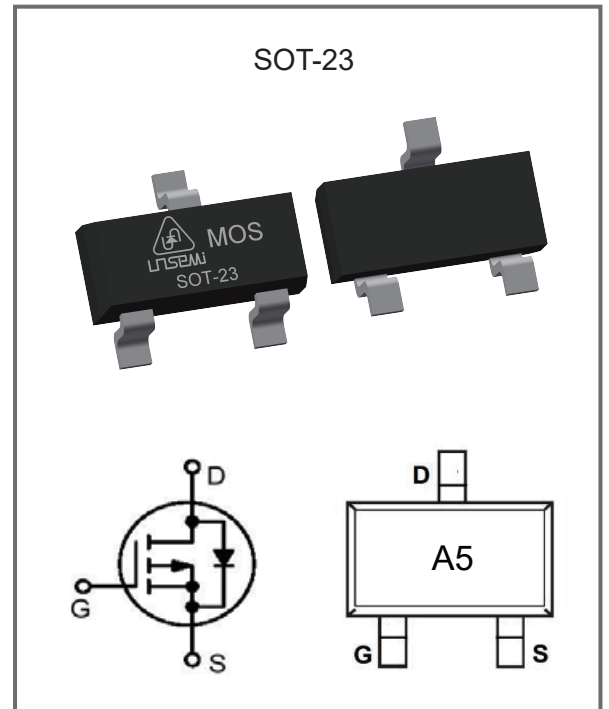
- ◆ Load Switch for Portable Devices
- ◆ Voltage controlled small signal switch

Package Marking And Ordering information

Part Number	Package Type	Packaging	Reel(pcs)
UN205P23T	SOT-23	Tape & Reel	3000



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Absolute Maximum Ratings TA = 25°C unless otherwise specified

Parameter	Symbol	Maximum	Units
Drain-Source Voltage	V _{DS}	-20	V
Gate- Source Voltage	V _{GS}	±12	V
Continuous drain current	I _D	-4.1	A
Peak Drain Current, Pulsed ¹⁾	I _{DM}	-15	A
Maximum Power Dissipation ²⁾	P _D	TA = 25°C	1.25
		TA = 75°C	0.8
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-55~150	°C

Thermal Characteristics

Parameter	Symbol	Max	Units
Junction-to-Ambient Thermal Resistance (PCB mounted) ²⁾	R _{θJA}	100	°C/W
Junction-to-Ambient Thermal Resistance (PCB mounted) ³⁾	R _{θJA}	166	°C/W

Notes

- 1) Pulse width limited by maximum junction temperature.
- 2) Surface Mounted on FR4 Board, t ≤ 5 sec.
- 3) Surface Mounted on FR4 Board.

Electrical Characteristics at TA = 25°C unless otherwise specified

Parameter	Symbol	Test Conditions	Min	Typ	Max	Units
STATIC PARAMETERS						
Drain-Source Breakdown Voltage	BVDSS	VGS = 0V , ID = -250μA	-20			V
Zero Gate Voltage Drain Current 0	IDSS	VDS = -20V			-1	μA
		VGS = 0V		TJ=55°C	-10	μA
Gate Body Leakage	IGSS	VGS = ±12V , VDS = 0V			±100	nA
Gate Threshold Voltage	VGS(TH)	VGS = VDS , ID = -250μA	-0.4		-1.0	V
Drain-Source On-State Resistance ¹⁾	RDS(ON)	VGS = -4.5V , ID = -4.1A		46	52	mΩ
		VGS = -2.5V , ID = -3.0A		60	75	mΩ
Source-Diode PARAMETERS						
Max. Diode Forward Current	IS				-1.6	A
Diode Forward Voltage	VSD	IS = -1.6A, VGS = 0V		-0.8	-1.2	V
DYNAMIC PARAMETERS						
Forward Transconductance ¹⁾	gts	VDS = -5V, ID = -3.5A		6.5		S
Input Capacitance	Ciss	VGS = 0V VDS = -6V F = 1MHz		415		pF
Output Capacitance	Coss			223		pF
Reverse Transfer Capacitance	Crss			87		pF
Gate charge total	Qg	VDS = -6V, VGS = -4.5V, ID = -3.5A		5.8		nC
Gate to Source Charge	Qgs			0.85		nC
Gate to Drain Charge	Qgd			1.7		nC
Turn-On Delay Time	td(ON)	VGED = -4.5V, VDD = -6V ID = -1A , RG = 6Ω RL = 6Ω		13		ns
Turn-On Rise Time	tr			36		ns
Turn-Off Delay Time	td(OFF)			42		ns
Turn-Off Fall Time	tf			34		ns

Notes

1) Pulse test: pulse width <= 300us, duty cycle <= 2%

Electrical Characteristics Curves

Fig 1: Power Dissipation

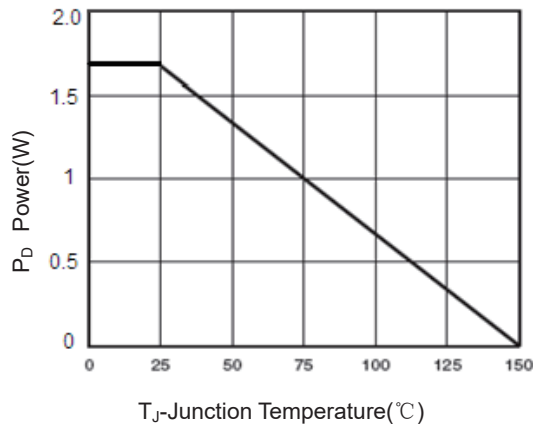


Fig 2: Drain Current

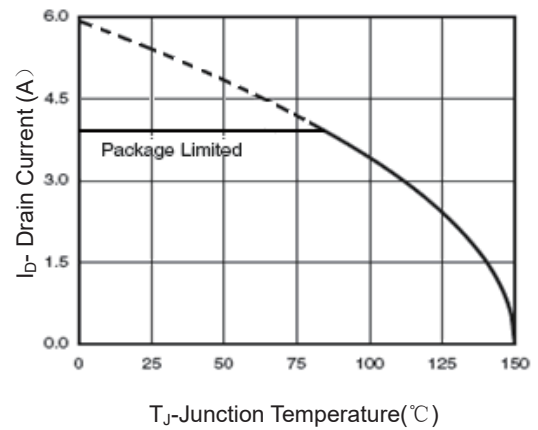


Figure 3: Output Characteristics

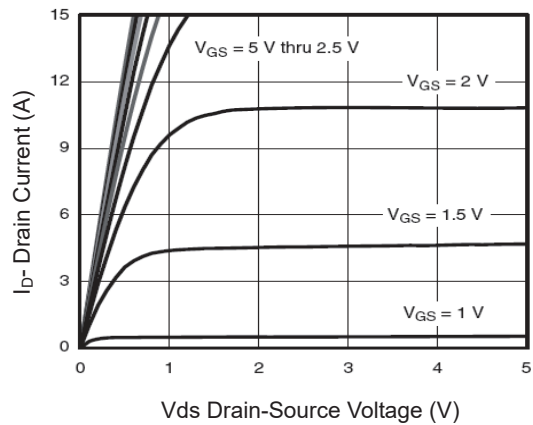


Figure 4: Drain-Source On-Resistance

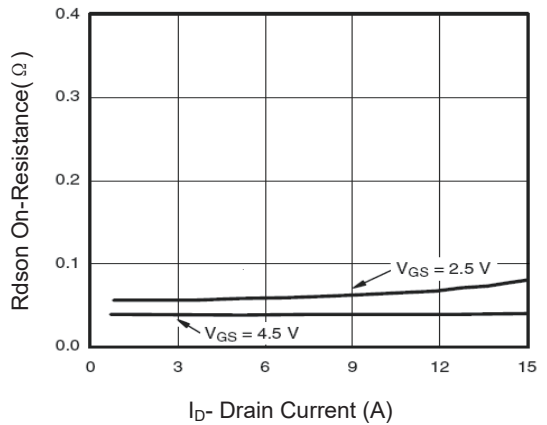


Figure 5: Transfer Characteristics

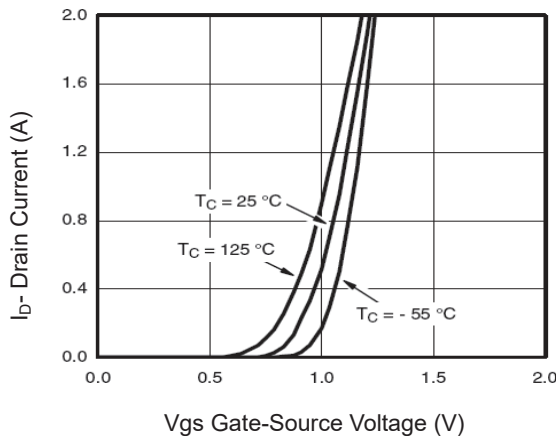
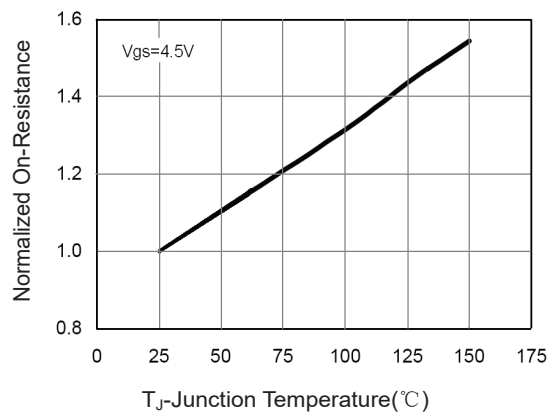


Figure 6: Drain-Source On-Resistance



Electrical Characteristics Curves

Figure 7: R_{ds(on)} vs V_{GS}

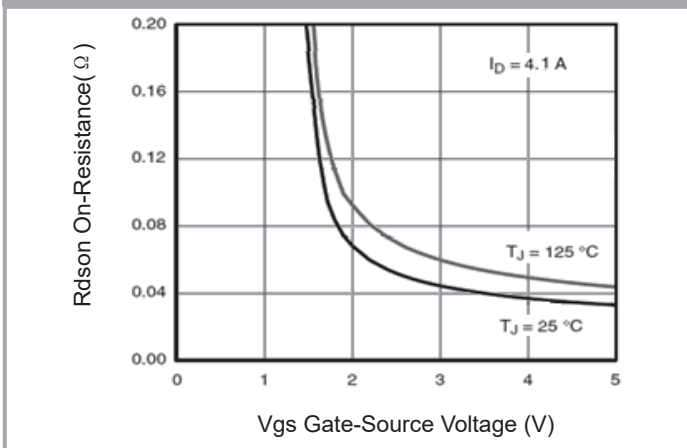


Figure 8: Capacitance vs V_{DS}

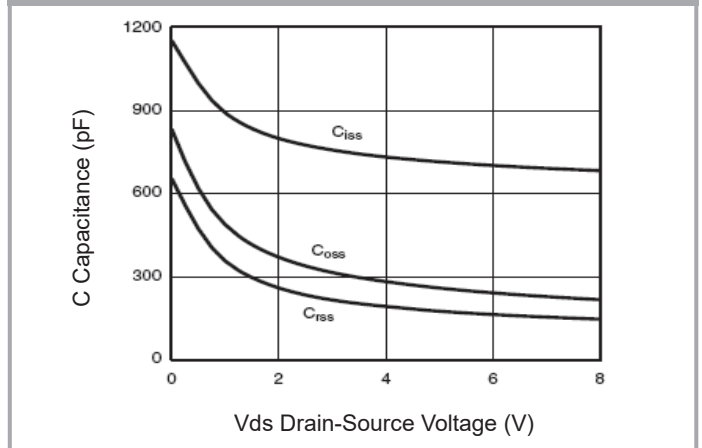


Figure 9: Gate Charge

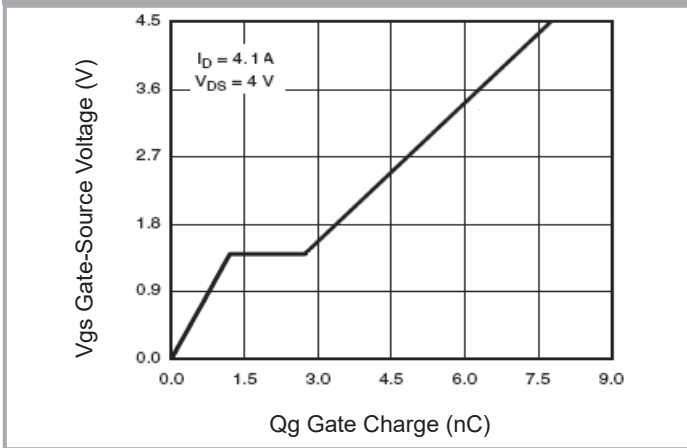


Figure 10: Source- Drain Diode Forward

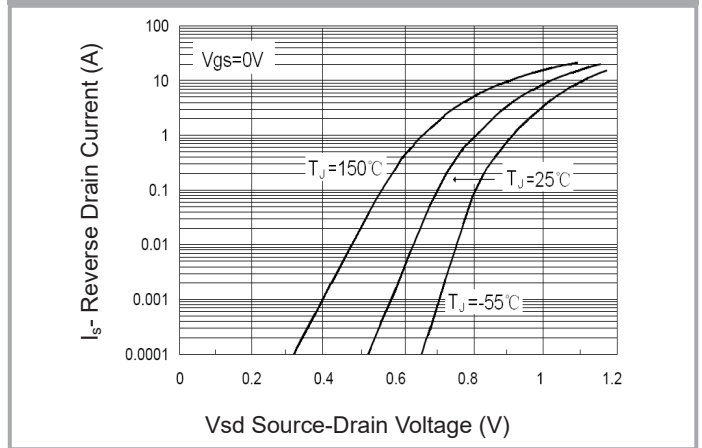
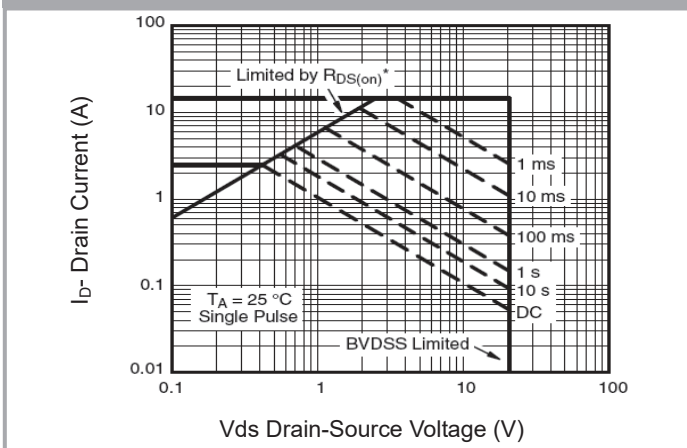
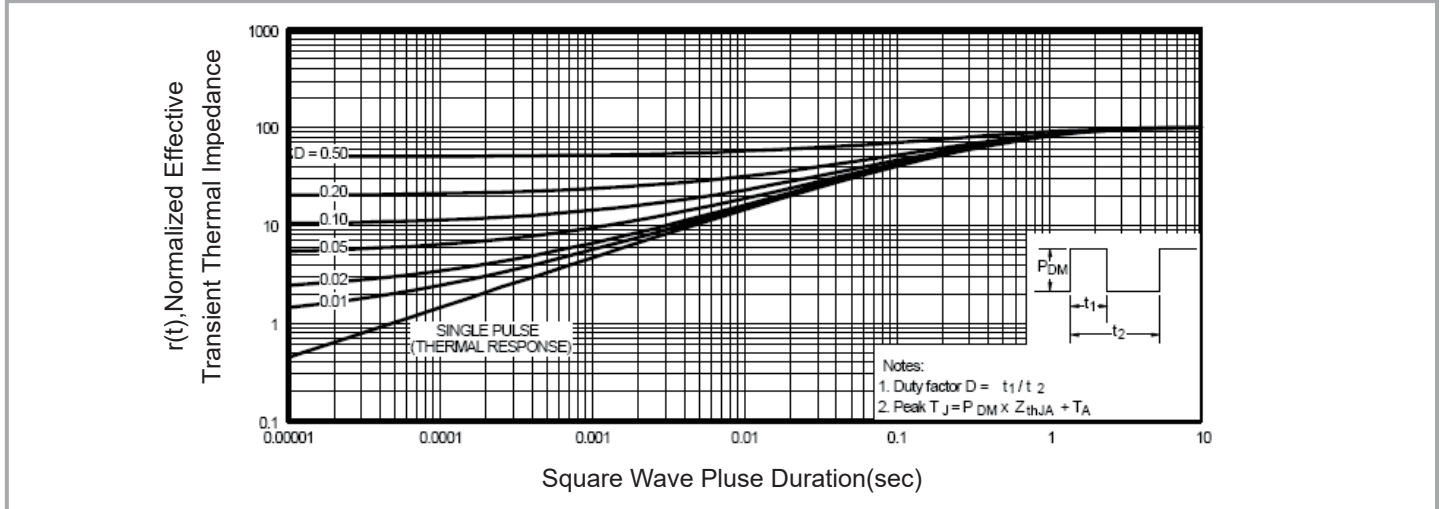


Figure 11: Safe Operation Area



Electrical Characteristics Curves

Figure 12: Normalized Maximum Transient Thermal Impedance



Test Circuit

Fig.1-1 Switching times test circuit

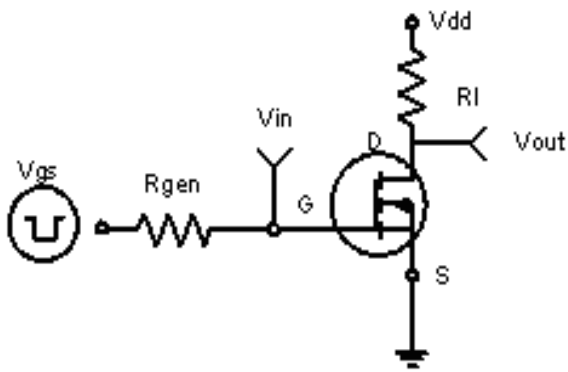
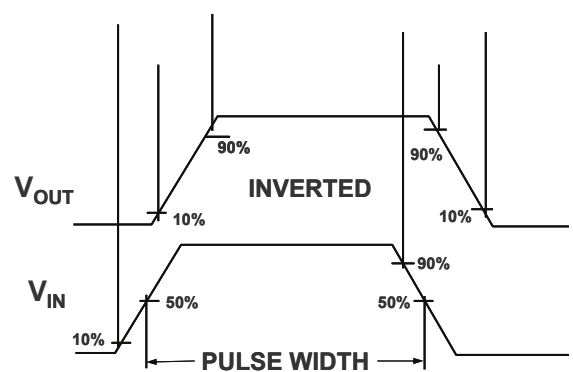
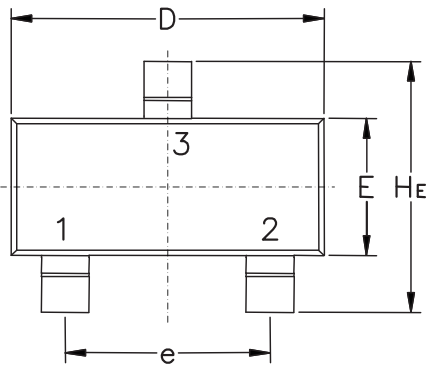


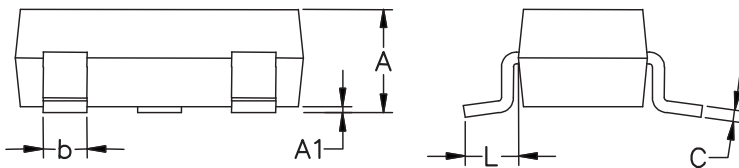
Fig.1-2 Switching Waveform



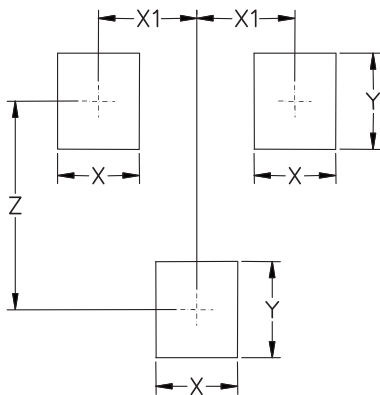
SOT- 23 Package Outline & Dimensions (Units: mm / in)



Symbol	Millimeters			Inches		
	Min.	Nom.	Max.	Min.	Nom.	Max.
A	1.05	1.11	1.25	0.042	0.044	0.050
A1	0.01	0.06	0.10	0.001	0.002	0.004
b	0.30	0.44	0.50	0.012	0.018	0.020
C	0.09	0.13	0.20	0.003	0.005	0.008
D	2.80	2.90	3.04	0.110	0.114	0.120
E	1.50	1.60	1.70	0.059	0.051	0.067
e	1.78	1.90	2.04	0.070	0.075	0.081
L	0.35	0.54	0.69	0.014	0.021	0.027
HE	2.65	2.80	2.95	0.104	0.112	0.116



Soldering Footprint



Symbol	Millimeters	Inches
X	0.80	0.031
X1	0.96	0.037
Y	0.90	0.035
Z	2.40	0.096

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